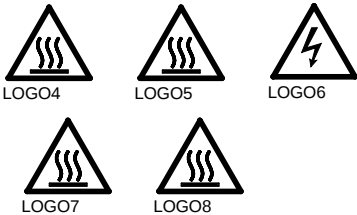


PCB Number: TIDA-01042
PCB Rev: A

Logo2
PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer



LBL1

PCB Label

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

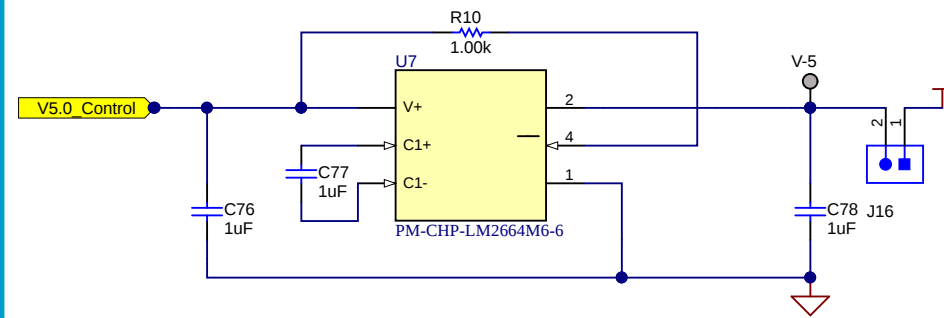
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Label Table	
Variant	Label Text
001	
002	

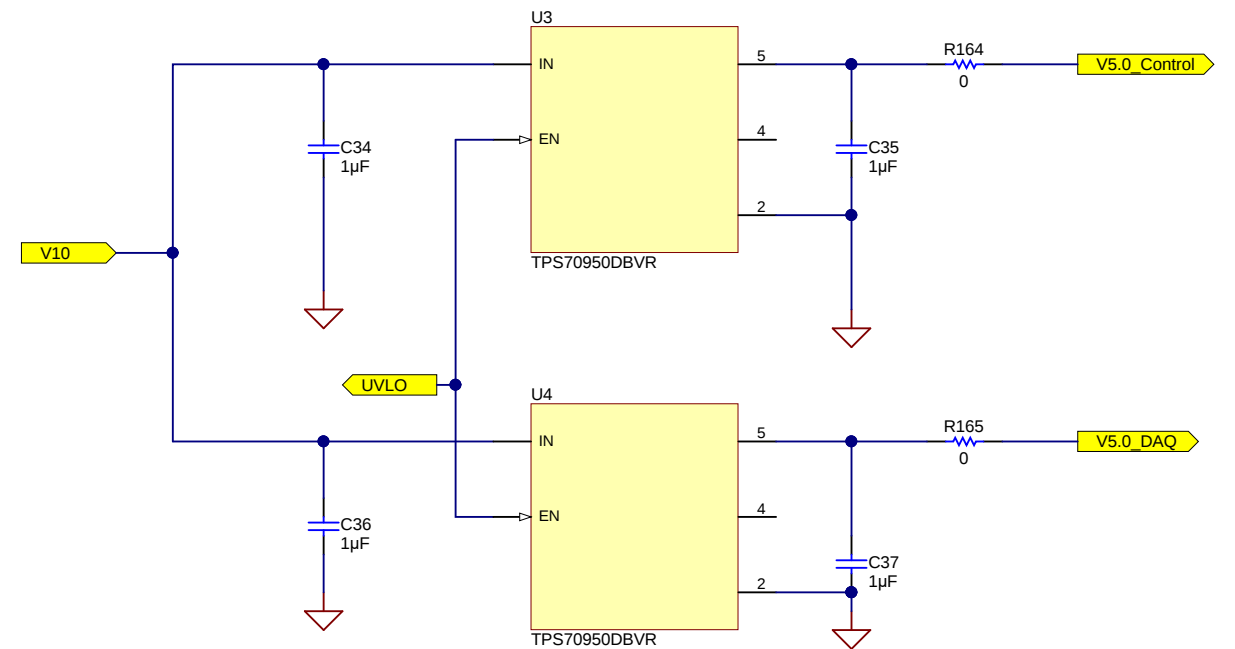
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TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A Applications	
Number: TIDA-01042	Rev: A	Sheet Title: Hardware
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 11
Drawn By:	File: SV601272A-HW.SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabet-Pour	Contact: http://www.ti.com/support	

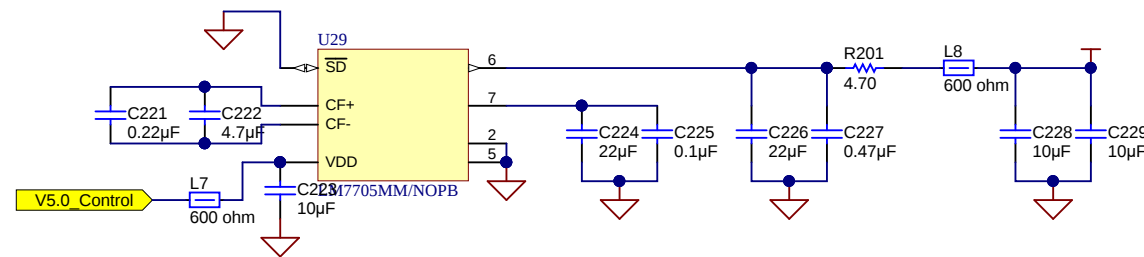
-5V Supply



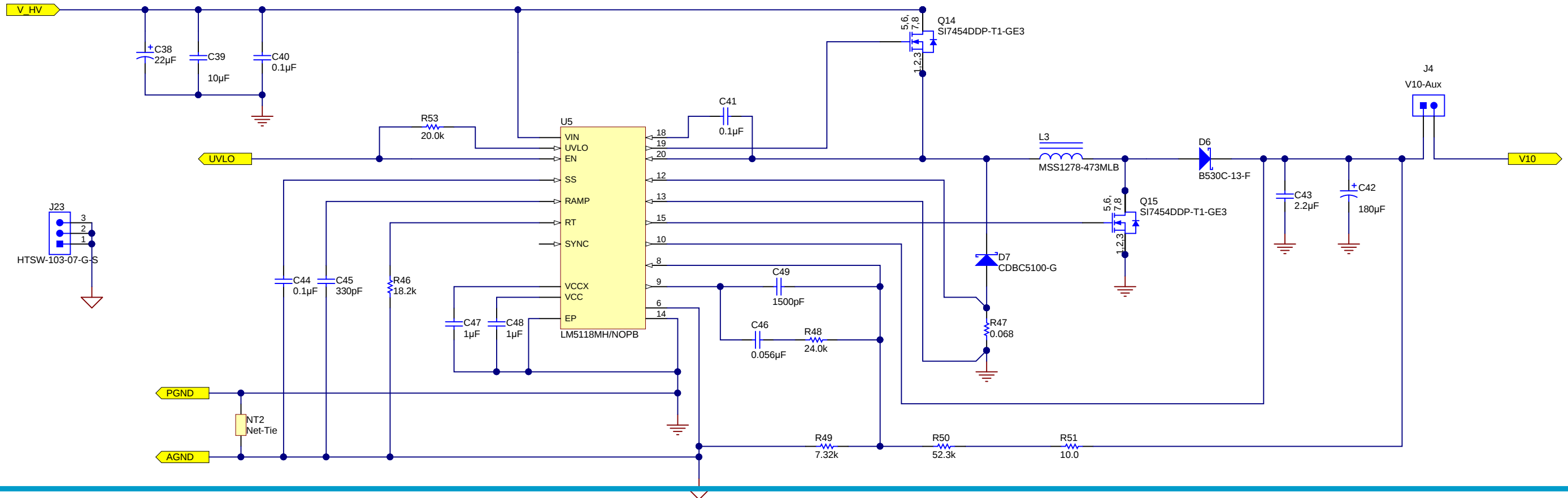
5V Supply



-0.2V Supply



10V Supply



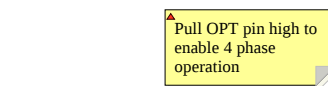
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TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A App	
Number: TIDA-01042	Rev: A	Sheet Title: Bias Supply
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 11
Drawn By:	File: SV601272A-Bias.SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabet-Pour	Contact: http://www.ti.com/support	

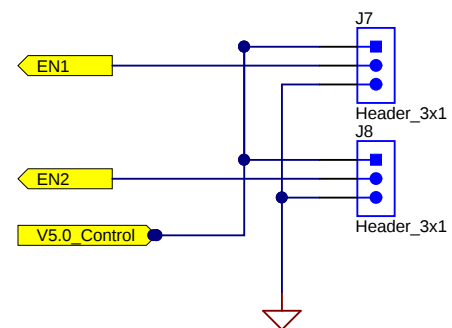
Pinout diagram for J28 connector:

Signal	Pin Number
OPT2	2
nFAULT2	4
IOUT3	6
SYNCOUT2	8
IOUT4	10
FB	12
SYNCIN2	14
UVLO	16
OVPB2	18
OVPA2	20

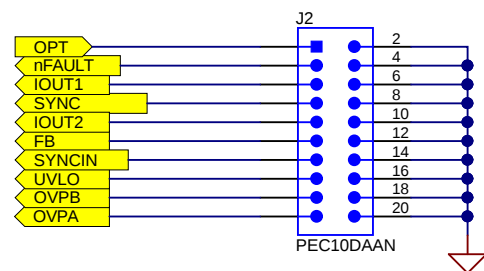
Connector: J28
Signal Source: PEC10DAAN



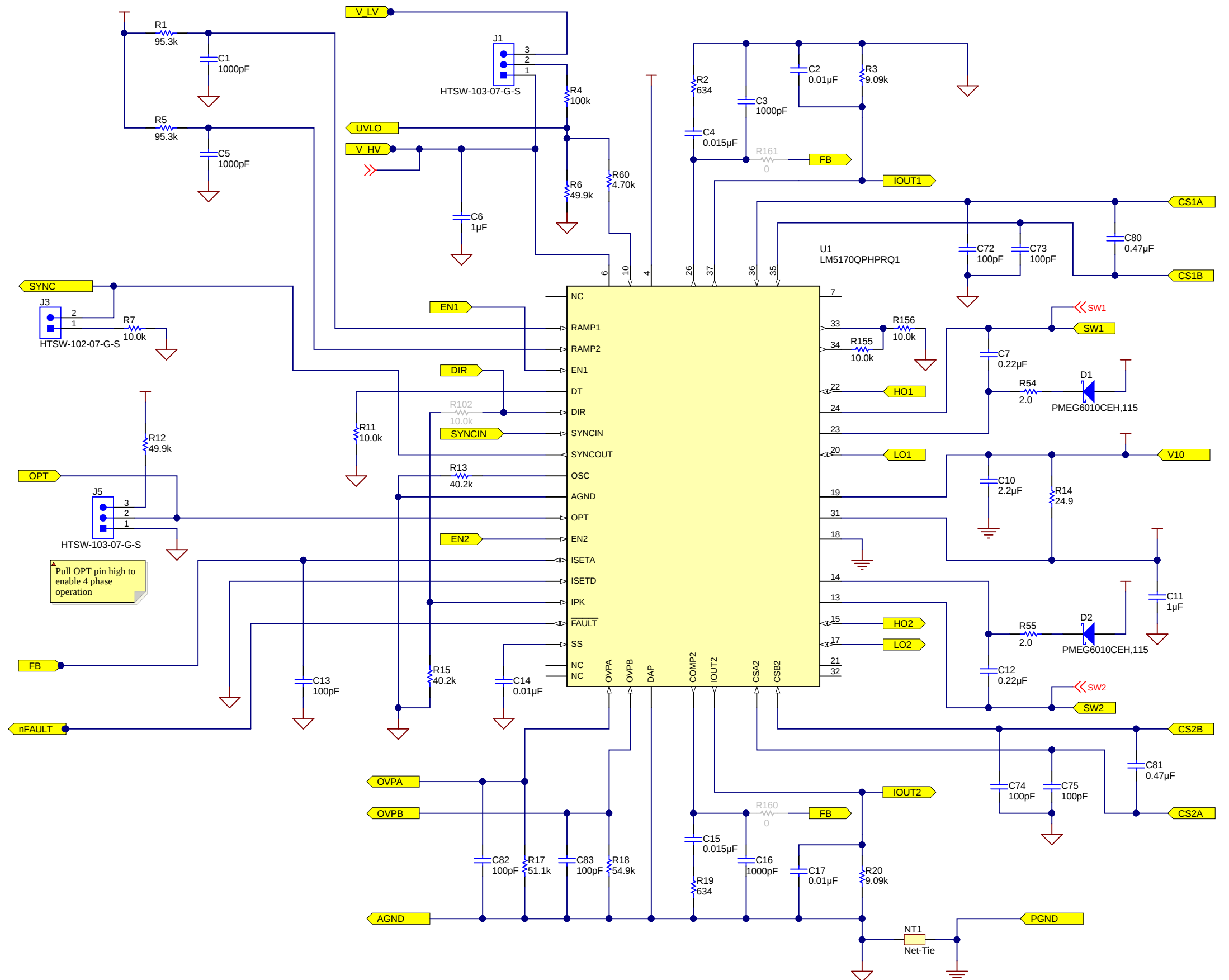
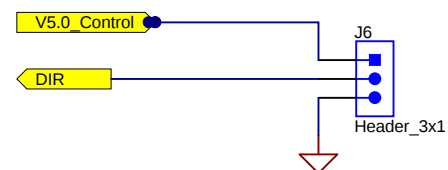
Enable Jumpers

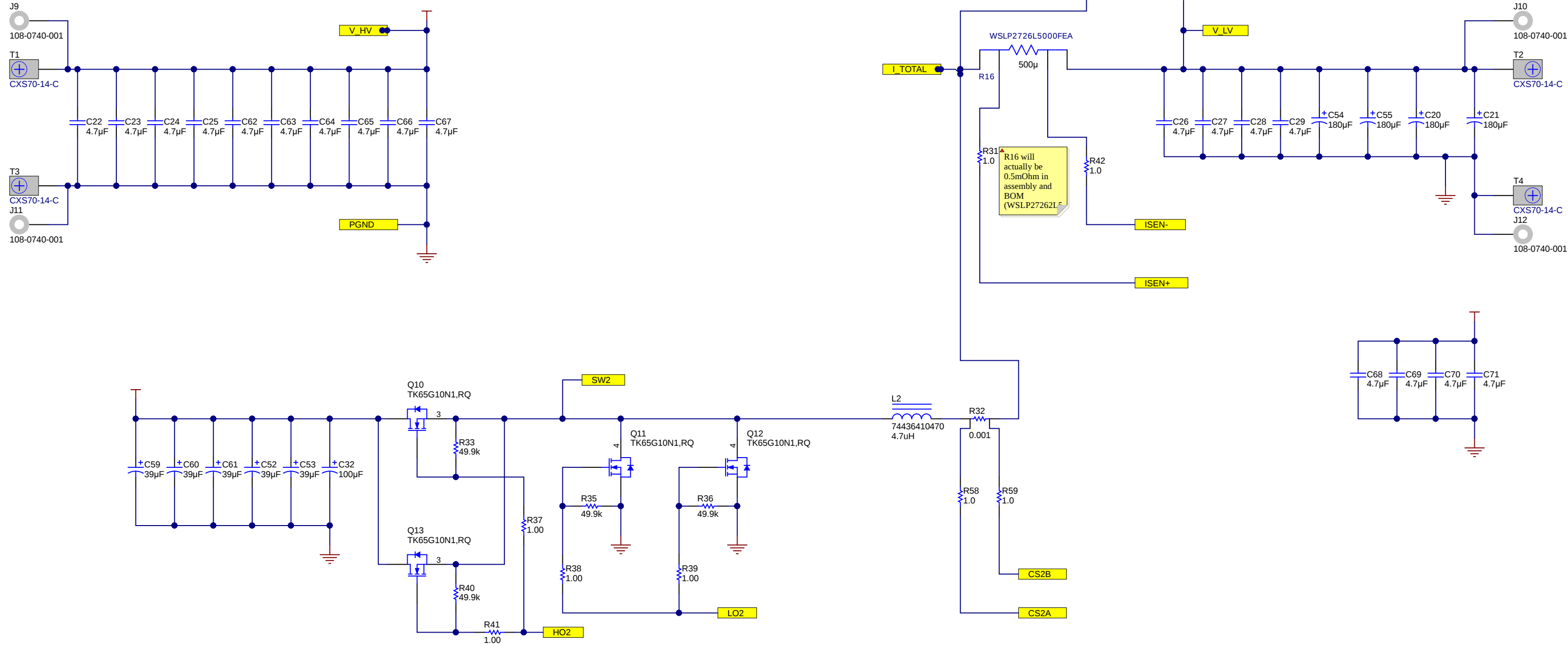


Optional Ground Jumpers

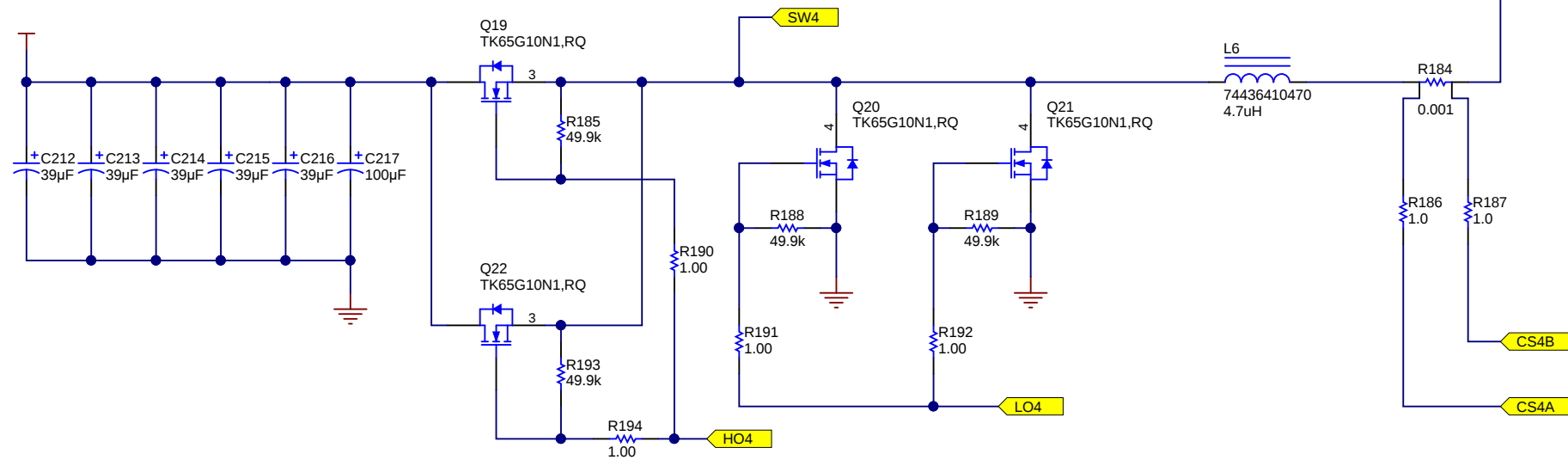
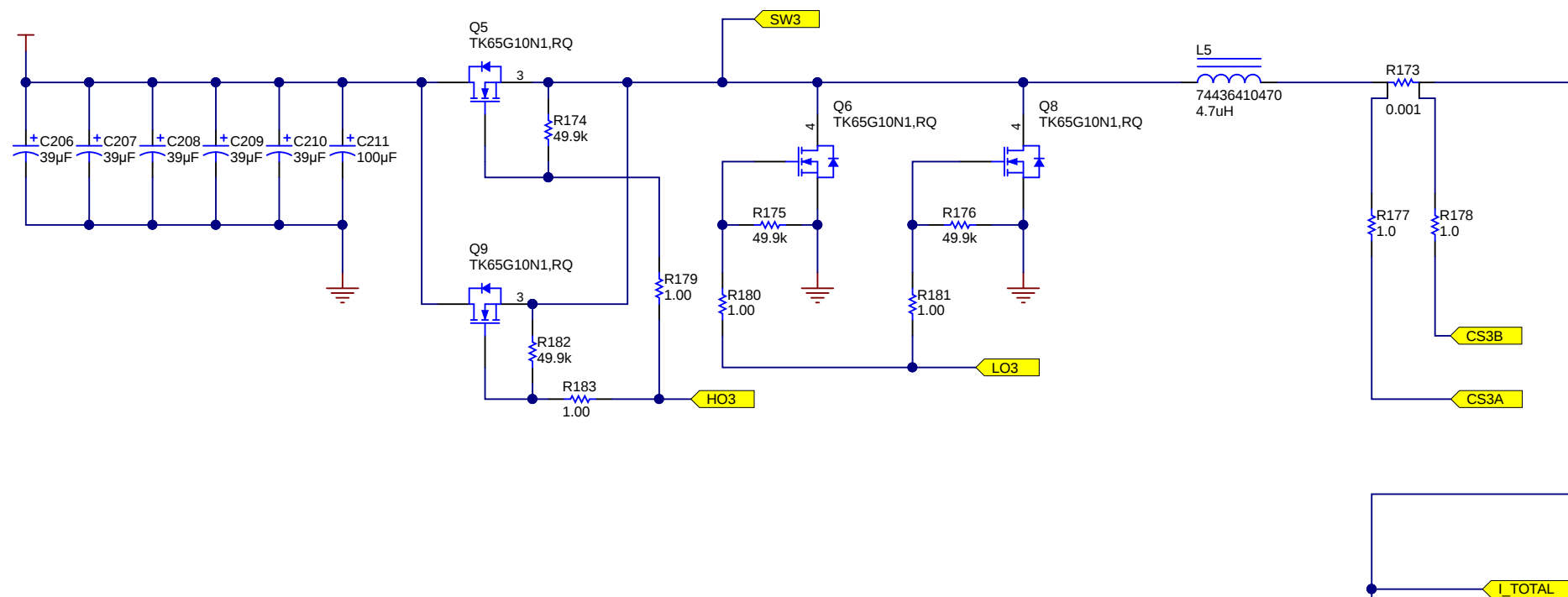


Direction Control



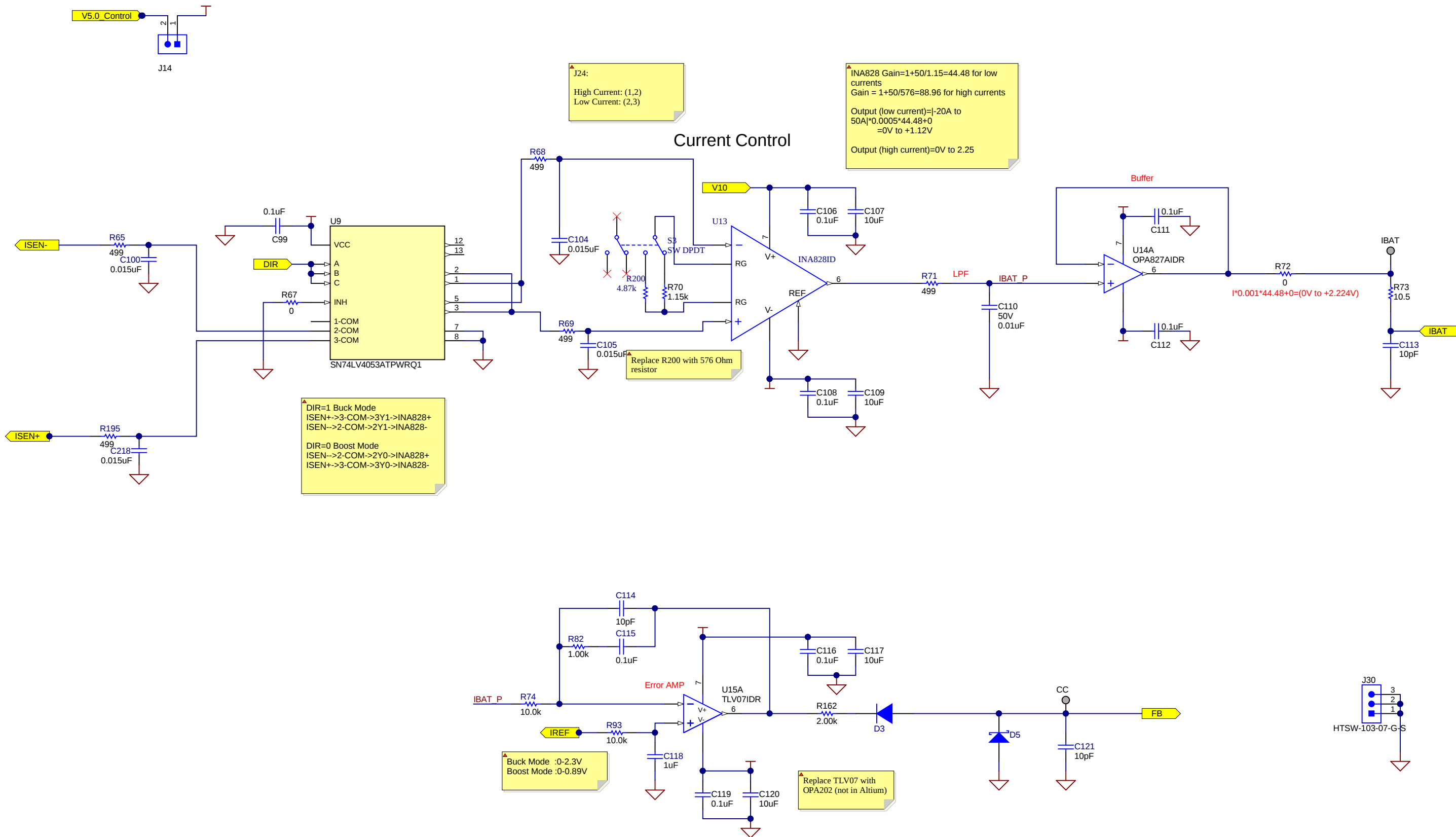


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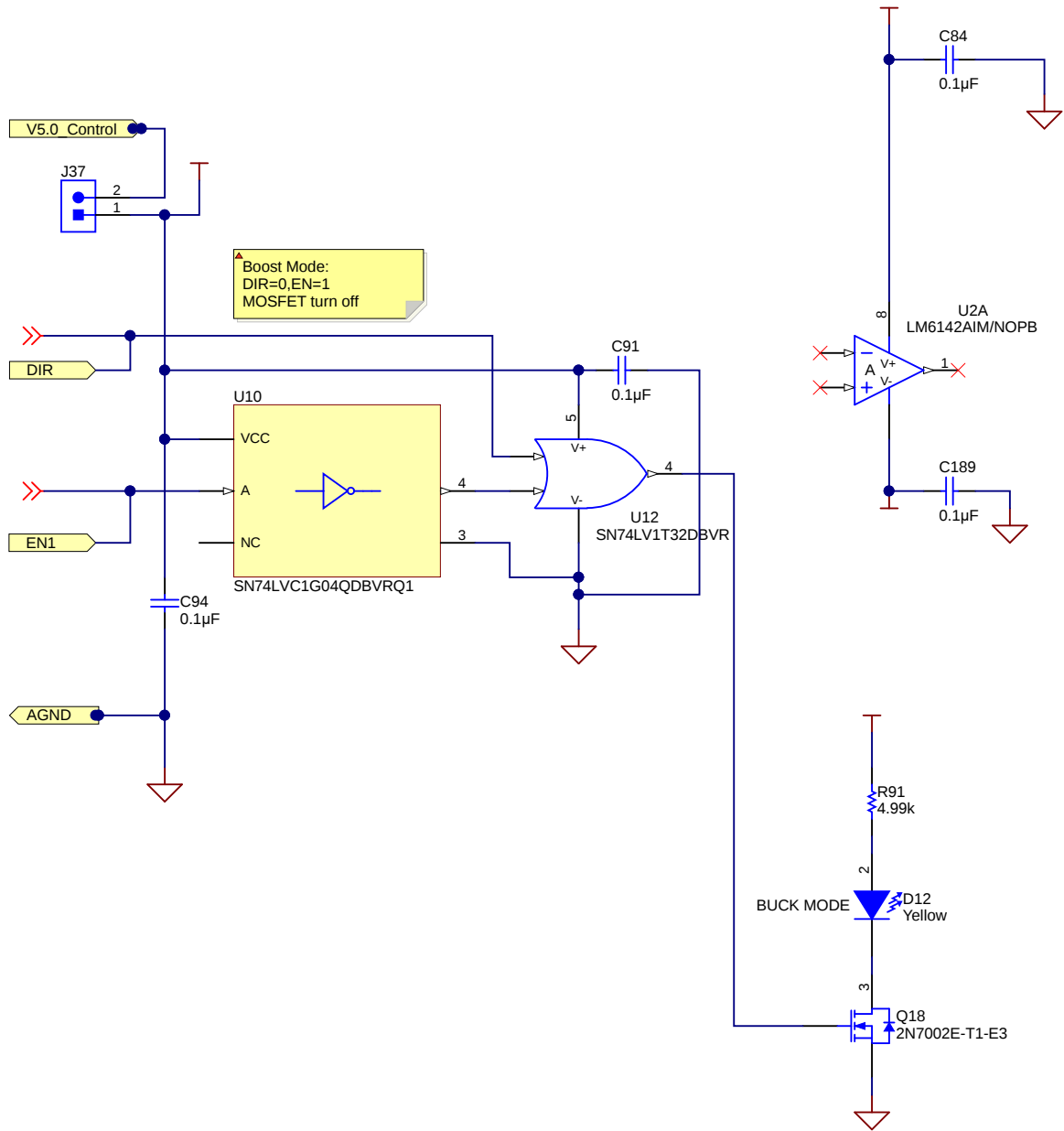
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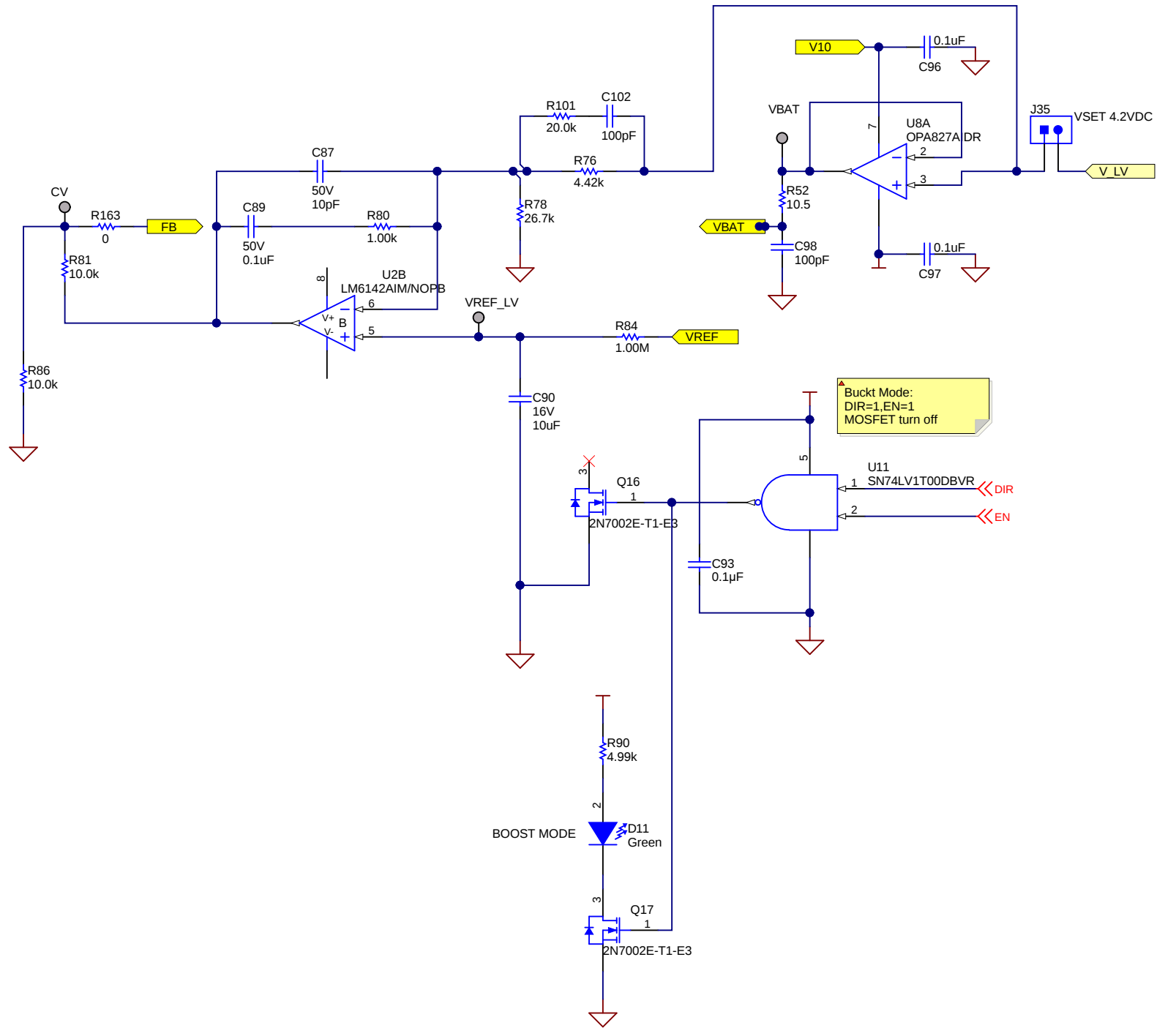


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BOOST CONTROL



BUCK CONTROL



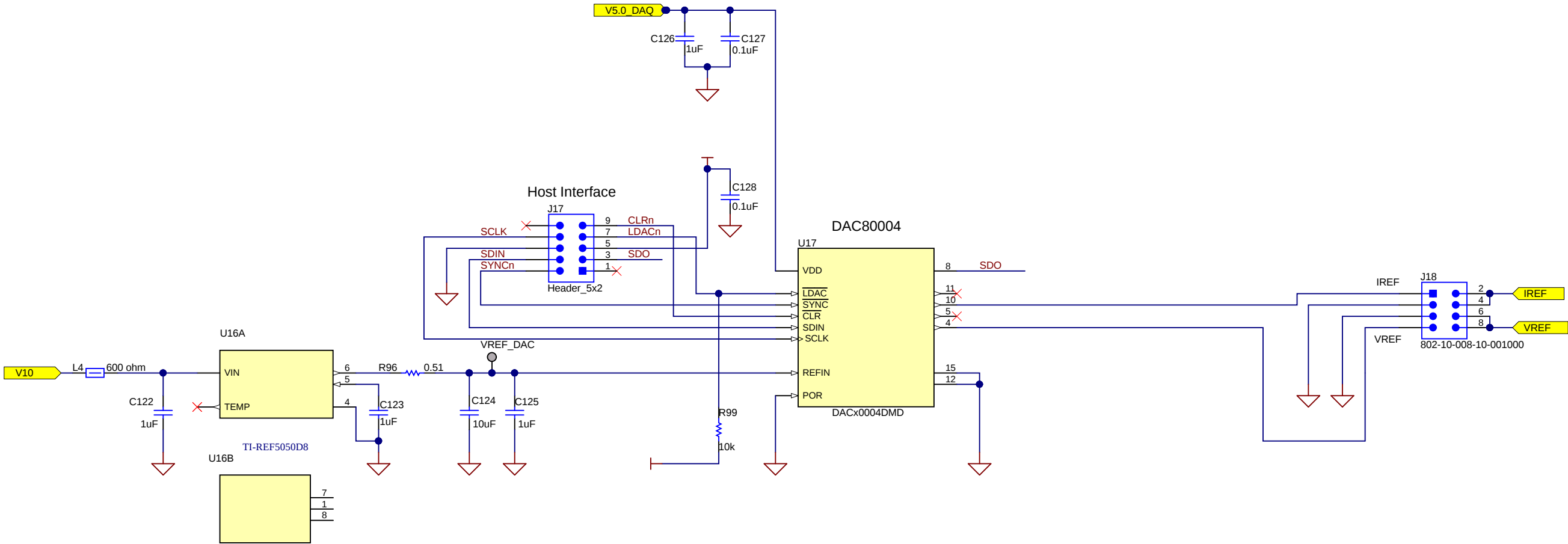
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TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A Applications	
Number: TIDA-01042	Rev: A	Sheet Title: Analog Loop Controller
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 7 of 11
Drawn By:	File: SV601272A-Analog.SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabet-Pour	Project: http://www.ti.com/support	

DAC

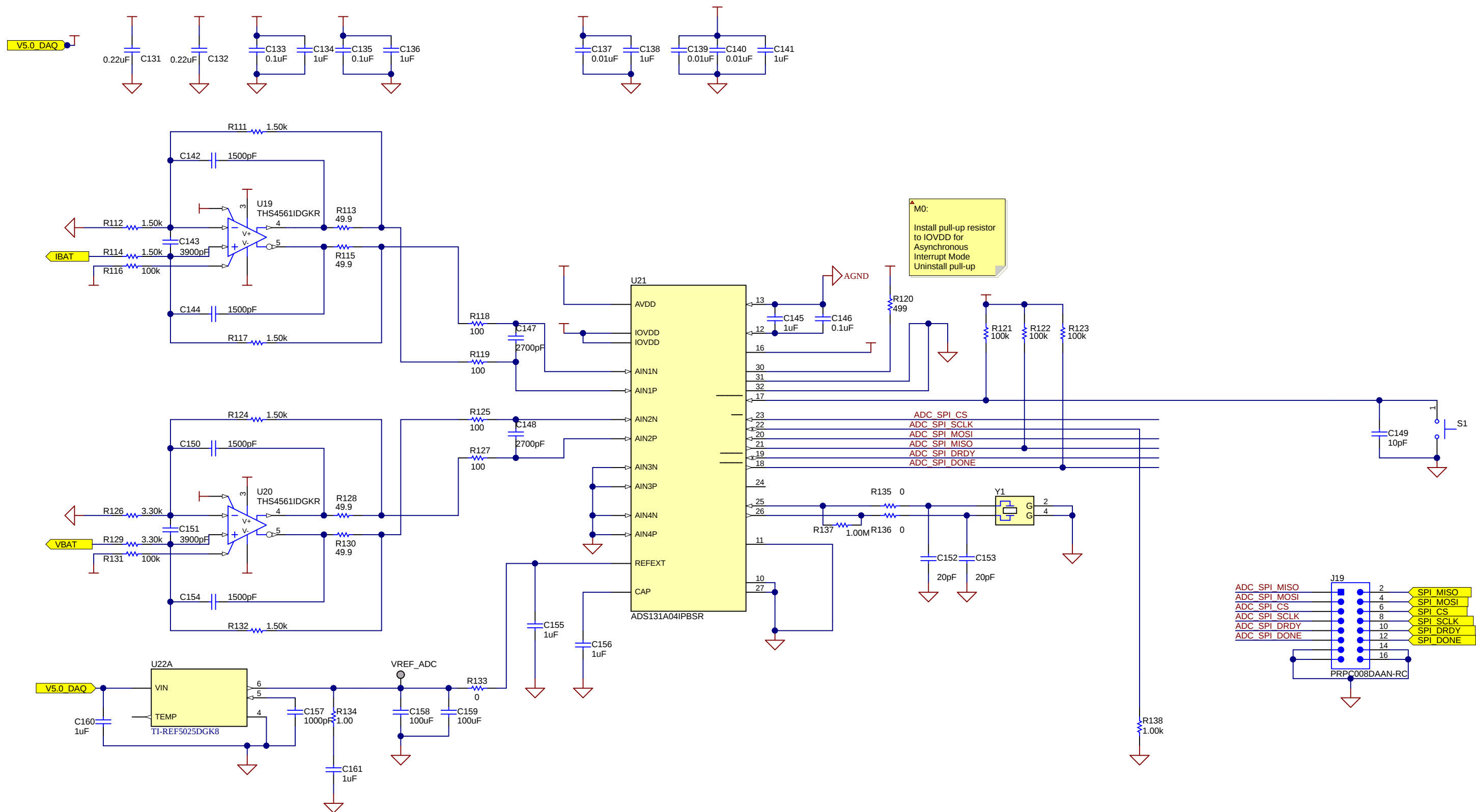
▲ IREF
Output Range:0V-2.3V

VREF
Output Range:0V-4.1V



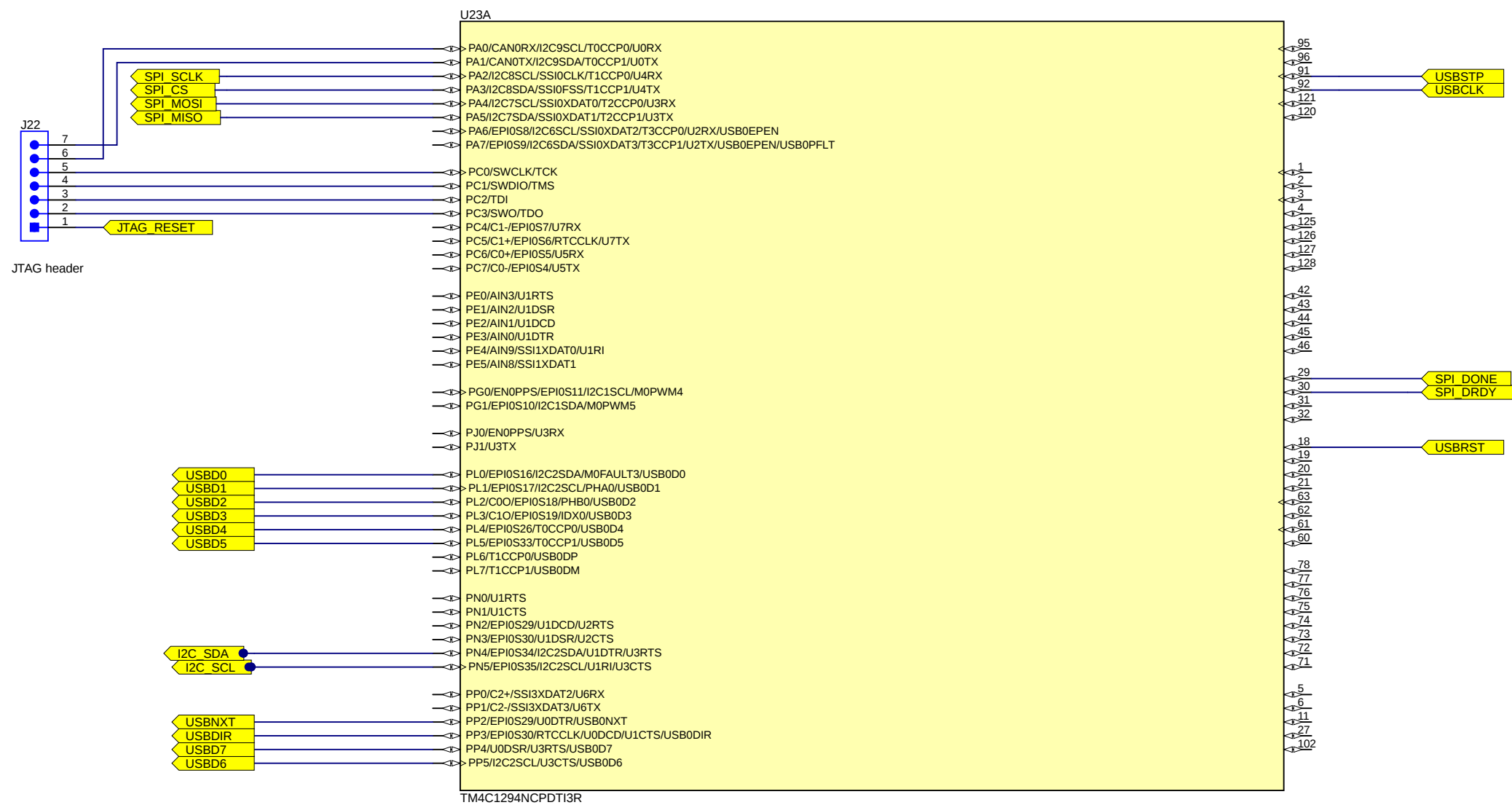
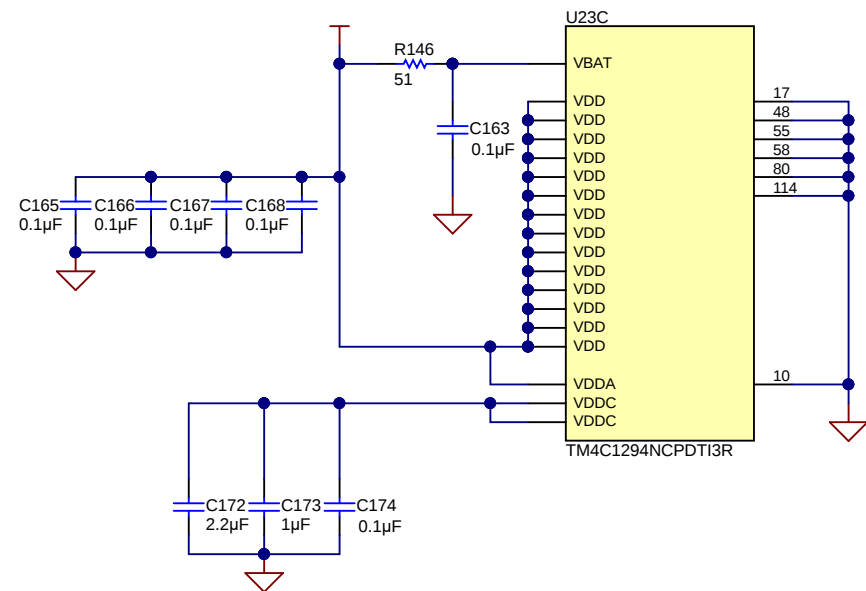
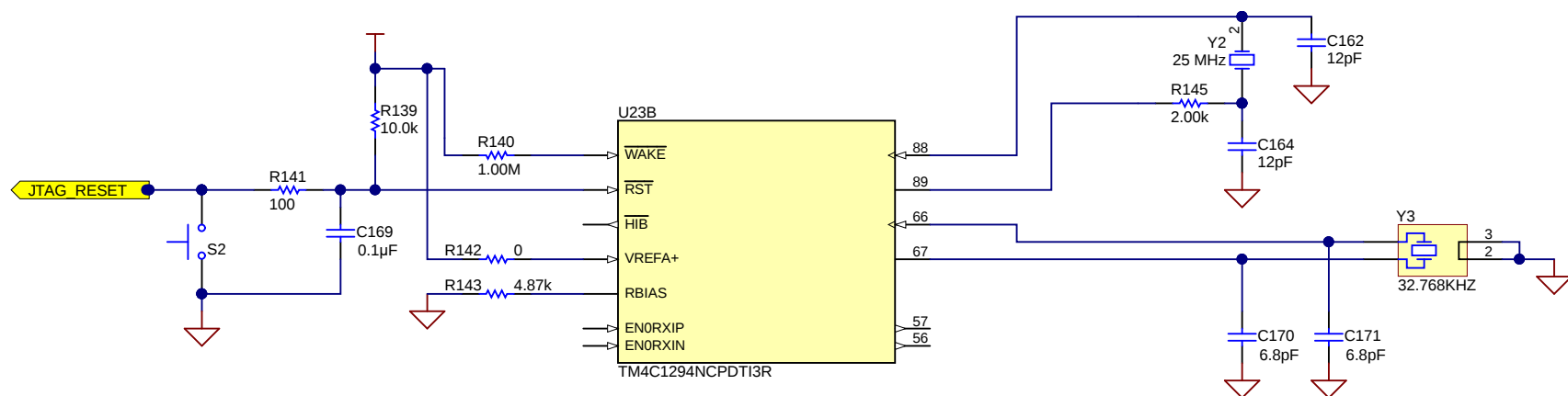
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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/4/2019
TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A Ap	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 8 of 11
Drawn By:	File: SV601272A-DAC.SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabet-Pour	Contact: http://www.ti.com/support	



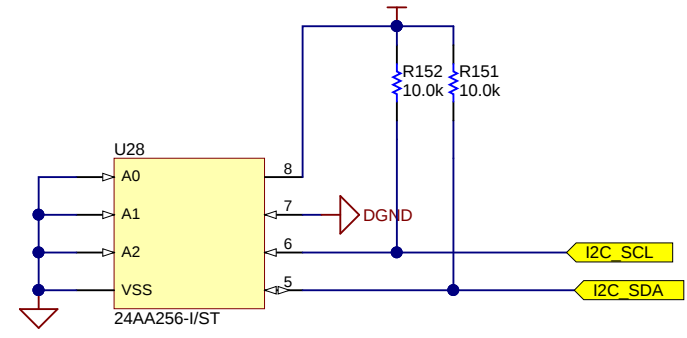
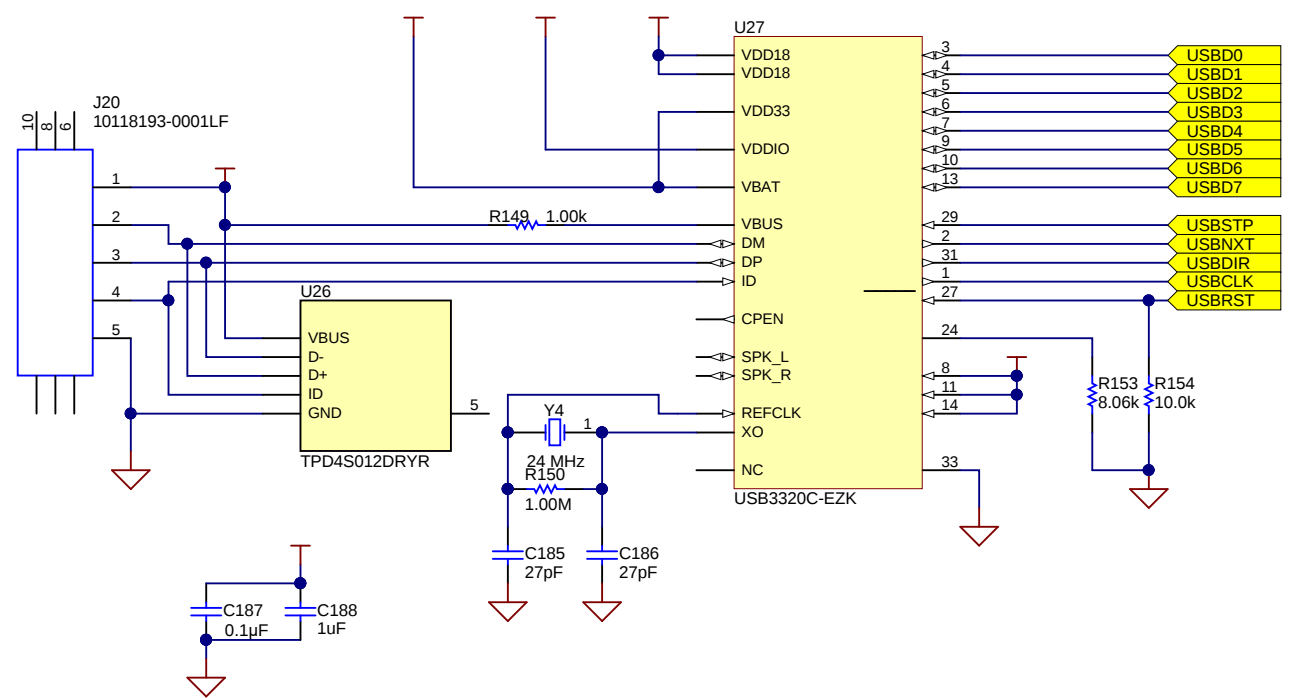
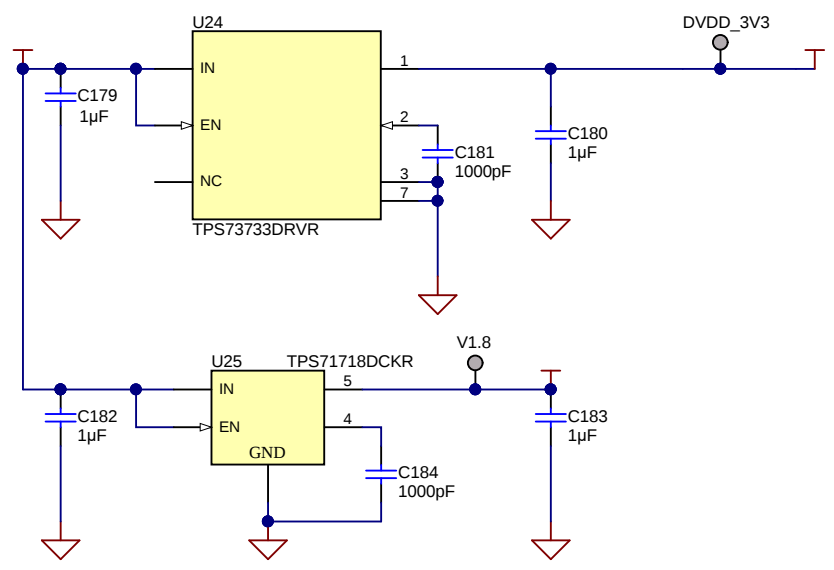
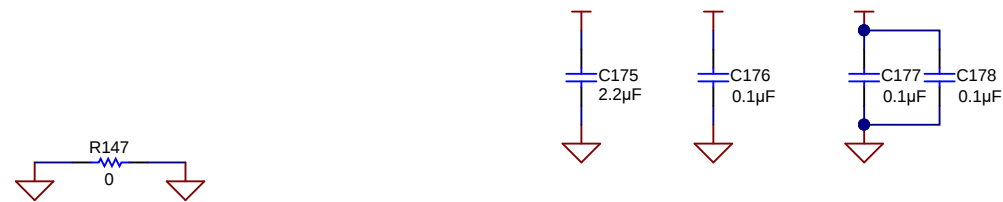
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TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A Applications	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 9 of 11
Drawn By:	File: SV601272A-ADC.SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabet-Pour	Contact: http://www.ti.com/support	



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TID #: TIDA-01042	Project Title: Modular Battery Tester for 50A, 100A, and 200A Appl	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 10 of 11
Drawn By:	File: SV601272A-TM4C_Main_SchDoc	Size: B
Engineer: Taras Dudar, Arash Sabat	Contact: http://www.ti.com/support	



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